



**INDIAN INSTITUTE OF INFORMATION TECHNOLOGY DESIGN AND
MANUFACTURING KANCHEEPURAM**

CHENNAI (TAMIL NADU) - 636 201

S M D P - C 2 S D

(Ministry of Electronics and Information Technology, Government of India)

Project position under SMDP-C2SD Project
(On Contract Basis)

Applications are invited for the post of **Lab Engineer** (purely on contract basis) under SMDP-C2SD Project funded by MeitY, Govt. of India.

The details are as under:

S.No	Designation	Number of Posts	Essential Qualification	Desirable	Consolidated Salary
1	Lab Engineer	One	B.Tech/B.E (ECE) / M.E / M.Tech. in VLSI Design with minimum 6.5 CGPA (or 60% marks)	Experience in VLSI / related area, working knowledge in VLSI tools like Cadence / Synopsys / Mentor / Xilinx, etc., publication in reputed Journals.	Rs.25,000 – 35,000 /- per month

Important Notes:

1. The duration of above post will be initially for **ONE YEAR** which may be re-advertised or extended on yearly basis to the maximum of project duration, subject to the satisfactory work, conduct, performance and as per provisions under the Project. The salary for above posts may be revised, as per the guidelines/provisions under this Project on annual basis.
2. No TA / DA will be paid for attending the Interview.
3. Interested candidates may **apply online** using the link given in Institute website from **23-May to 14-Jun-2018** and the shortlisted candidates will be **informed by 16-Jun-2018**. The shortlisted candidates may report on **21-Jun-2018 at 9 AM in Administrative Building (Discussion Room) of IITDM Kancheepuram** along with Application Copy, resume and attested copies of the degree certificates, mark sheets and other testimonials.
4. A written test / Interview on **Semi-Conductor devices and Analog & Digital Circuits** will be conducted as part of the selection process.


Signature

Project Investigators

SMDP-C2SD Project


22/5/18