

**List of Candidates shortlisted for Test/ Interview to be held on 13.12.2011 for admission to
Ph.D. in Computer Science and Engineering - Jan 2012**

S. No	Application No	Name of Candidate
1.	Ph. D/12 - CS-06	A. Sivanesh Kumar
2.	Ph. D/12 - CS-07	S. Britto Raj
3.	Ph. D/12 - CS-08	M. Mohamed Asan Basiri
4.	Ph. D/12 - CS-10	Baktavatchalam. G
5.	Ph. D/12 - CS-12	T. Raja Sree
6.	Ph. D/12 - CS-17	N. Rajganesh
7.	Ph. D/12 - CS-22	C. Gunasundari
8.	Ph. D/12 - CS-34	Nazeer Shaik
9.	Ph. D/12 - CS-40	Karthick. K
10.	Ph. D/12 - CS-45	Kumar Dorti
11.	Ph.D/12 - EE-12	R. Ganga
12.	Ph.D/12 - EE-18	N. Karthick Rajan

Note:

1. Reporting time is 10 AM on Tuesday, 13.11.2011.
2. Written Test would be held from 10.30 to 11.30 AM.
3. Candidates are required to bring / provide the following at the time of written test and interview :
Original (for verification) of 10th, 12th, Under Graduate, Post Graduate mark sheets, GATE/NET/CAT/JMAT or equivalent score card, category certificates (if applicable).
4. **Selected candidates need to send acceptance together with the fees for the first semester on or before 23 December 2011 in person or by speed post. Admission and Orientation Programme for selected candidates and commencement of classes is on 02.01.2012**

AREAS FOR THE WRITTEN TEST / INTERVIEW

Candidates with PG in CS	Candidates with PG in Electronics
<ol style="list-style-type: none"> 1. Data Structures & Algorithms 2. C Programming 3. Computer Organization 4. Discrete Mathematics 5. Operating Systems 	<ol style="list-style-type: none"> 1. Data Structures & Algorithms 2. C Programming 3. Computer Organization 4. Image Processing 5. Digital Signal Processing

List of Candidates shortlisted for Written Test and Interview to be held on 13.12.2011 for admission to Ph.D. in Electronics Engineering – Jan 2012

S.No	Application No	Name of Candidate
1.	Ph.D/12-EE-13	A.Ananth
2.	Ph.D/12-EE-15	Mervin. J
3.	Ph.D/12-EE-16	Ditto Antony
4.	Ph.D/12-EE-19	Chandrakala. K
5.	Ph.D/12-EE-23	Uma S.
6.	Ph.D/12-EE-26	Abdul Majeed K.K
7.	Ph.D/12-EE-43	Satish Kumar Sanala
8.	Ph.D/12-EE-47	Logesh. K
9.	Ph.D/12-EE-55	Rajalingam
10.	Ph.D/12-EE-63	M. Navaneetha Velammal

Note:

1. Reporting time is 10:00 AM on Monday 13.12.2011.
2. Written Test of duration One hour would be held from 10.30 AM to 11.30 AM.
3. Candidates are required to bring / provide the following at the time of written test and interview :
Original (for verification) of 10th, 12th, Under Graduate, Post Graduate mark sheets, GATE/NET/CAT/JMAT or equivalent score card, category certificates (if applicable).
4. **Selected candidates need to send acceptance together with the fees for the first semester on or before 23 December 2011 in person or by speed post. Admission and Orientation Programme for selected candidates and commencement of classes is on 02.01.2012**

SYLLABUS FOR WRITTEN TEST

1. SEMICONDUCTOR DEVICES
2. ELECTRIC NETWORKS
3. SIGNALS AND SYSTEMS
4. ANALOG & DIGITAL CIRCUITS

**List of Candidates shortlisted for Interview to be held on 14.12.2011
for admission to Ph D in Mechanical Engineering - Jan 2012**

S.No	Application No.	Name of the Candidate
1.	Ph. D/12-ME-05	Palaniammal. R
2.	Ph. D/12-ME-09	R. Senthil Kumar
3.	Ph. D/12-ME-10	S. M. Vadivel
4.	Ph. D/12-ME-14	R. Rajasekaran
5.	Ph. D/12-ME-15	Navaneetha Krishnan. G
6.	Ph. D/12-ME-16	V. Lakshmanan
7.	Ph. D/12-ME-19	Jagadeesh. D

Note:

1. Reporting time is 10 AM on Wednesday, 14.12.2011.
2. Candidates are required to bring the following at the time of interview :
 - a. Original (for verification) of 10th, 12th, Under Graduate, Post Graduate mark sheets,
 - b. GATE/NET/CAT/JMAT or equivalent score card, category certificates (if applicable).
3. **Selected candidates need to send acceptance together with the fees for the first semester on or before 23 December 2011 in person or by speed post. Admission and Orientation Programme for selected candidates and commencement of classes is on 02.01.2012**